ZK-S12-B
Starter Kit for Freescale HCS12(X) Family (80-Pin QFP ZIF Socket)

User's Manual

1. Introduction

Overview

The ZK-S12-B Starter Kit has been designed for the evaluation of the 80-pin QFP Freescale HCS12(X) family and the debugging of user applications. The ZK-S12-B Starter Kit can be used as a standalone application, via its built-in USB to BDM interface, or together with an external debugger through a BDM-compatible connection.

Starter Kit Features

The ZK-S12-B Starter Kit features the following sections.

1. An "MCU" section containing:

    ▪ An MC9S12DJ256 microcontroller (in 80-pin QFP package, already programmed with a demo application—in addition, you can also use any other pin-to-pin compatible device);
    ▪ ZIF socket for the microcontroller;

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- A connector area to access the I/O pins of the microcontroller for expansion prototyping;
- Three clock sources: a provision for a clock module, a 16 MHz crystal in Pierce configuration and a 16 MHz crystal in Colpitts configuration, selectable via the **"OSC SEL"** and **"XCLK#=0"** jumpers;
- A jumper (**"XFC SEL"**) allowing either the provided RC loop filter (needed for the microcontroller's internal PLL) or a user-made RC loop filter to be enabled;
- Two jumpers (**"VRH SEL"** and **"VRL SEL"**) which allow the high and low voltage reference for the MCU internal A/D converter to be defined;
- A **"VREGEN"** jumper allowing the on-chip voltage regulator to be enabled or disabled;
- Two jumpers (**"MODA=1"** and **"MODB=1"**) which allow the MCU operating mode upon reset to be defined.

2. A "POWER SUPPLY" section which accepts a 12 V DC voltage (used for the LIN and CAN transceivers) and, thanks to the built-in switching power supply, provides a regulated VDD voltage for the rest of the board. A jumper (**"VDD SEL"**) allows two different microcontroller VDD voltages (3.3 V or 5.0 V) to be selected. An additional linear power supply regulator provides the 5.0 V voltage required by the "USB TO BDM INTERFACE" section.

3. A built-in "USB TO BDM INTERFACE" section which allows the host PC to communicate with the microcontroller through a standard USB interface. USB 2.0 is fully supported. When using an external in-circuit debugger (via the BDM connector), the "USB TO BDM INTERFACE" circuitry must be bypassed by removing the **"RESET ENA"** and **"BKGD ENA"** jumpers.

4. A standard BDM connector for in-circuit debugging/programming;

5. A "RESET" section containing the reset supervisor circuitry and a push-button connected to the MCU Reset pin. The reset supervisor circuitry (enabled by default) can be disabled by removing the **"LVD ENA"** jumper.

6. An "INPUTS" section containing:

- Four push-buttons, connected to PP0, PP1, PP2 and PP3;
- Eight general-purpose DIP-switches connected to Port T;
- A potentiometer, together with a jumper to connect/disconnect to/from PAD00.

7. An "OUTPUTS" section containing eight high-efficiency (low-current) LEDs connected to Port B, together with eight jumpers to connect/disconnect each of the eight LEDs to/from their respective Port B pins.

8. A "RS-232" section providing two RS-232 channels connected to the microcontroller's SCI0 and SCI1 serial communication interfaces. Each RS-232 channel can be configured as DTE (Data Transmission Equipment) or DCE (Data Communication Equipment) via the relative jumper (**"RS-232 x DTE/DCE SEL"**). The microcontroller's RX and TX lines used by the RS-232 transceivers are shared with the LIN trasceivers' RX and TX lines, respectively. Use the **"RXDx SEL"** and **"TXDx SEL"** jumpers in the "SERIAL

SETTINGS" section of the board to select whether to use the RS-232, LIN, or to free the microcontroller's RX and TX lines. Two 9-pin, D-Sub female connectors are provided for each RS-232 channel.

9. The "LIN" section contains two LIN transceivers, each capable of a speed of up to 100 Kbps in fast mode. Every LIN node can be configured as a master node via its respective **"MASTER NODEx ENA"** jumper, which inserts a 900 Ohm resistor between the LIN bus line and the LIN transceiver power supply line. The LIN transceivers can be powered either by the Starter Kit's internal 12 V DC reference, or by the LIN network itself, via the **"TRANSCEIVER SUPPLY SEL"** jumper. Analogously, the LIN network can be supplied by the Starter Kit's internal 12 V DC reference via the **"LIN SUPPLY ENA"** jumper. The microcontroller's RX and TX lines used by the LIN transceivers are shared with the RS-232 transceivers' RX and TX lines, respectively. Use the **"RXDx SEL"** and **"TXDx SEL"** jumpers in the "SERIAL SETTINGS" section of the board to select whether to use the RS-232, LIN, or to free the microcontroller's RX and TX lines associated with these nodes. Two 3x1 male header connectors are provided to interface to an external LIN bus.

10. The "SERIAL SETTINGS" section has four jumpers that allow the uses of the SCI0 and SCI1 peripherals of the microcontroller to be chosen. The SCI0 peripheral can be connected to the LIN0 node or to the RS-232 channel 0, or can be freed by removing all jumpers. The SCI1 peripheral can be connected to the LIN1 node or to the RS-232 channel 1, or can be freed by removing all jumpers.

11. The "CAN" section contains two fault-tolerant (up to 125 Kbaud) CAN transceivers. The TX and RX signals of CAN nodes CAN0 and CAN4 can be disconnected (by removing the respective **"CANx TX ENA"** and **"CANx RX ENA"** jumpers) from the microcontroller's respective pins. Two 3x1 male header connectors are provided to interface to an external CAN bus.

12. A prototype area features both a standard, thru-hole area (for mounting traditional components) and a SMD area (for soldering SMD components). Additionally, all of the board's supply lines (12 V, 5 V, VDD and GND lines) are provided.
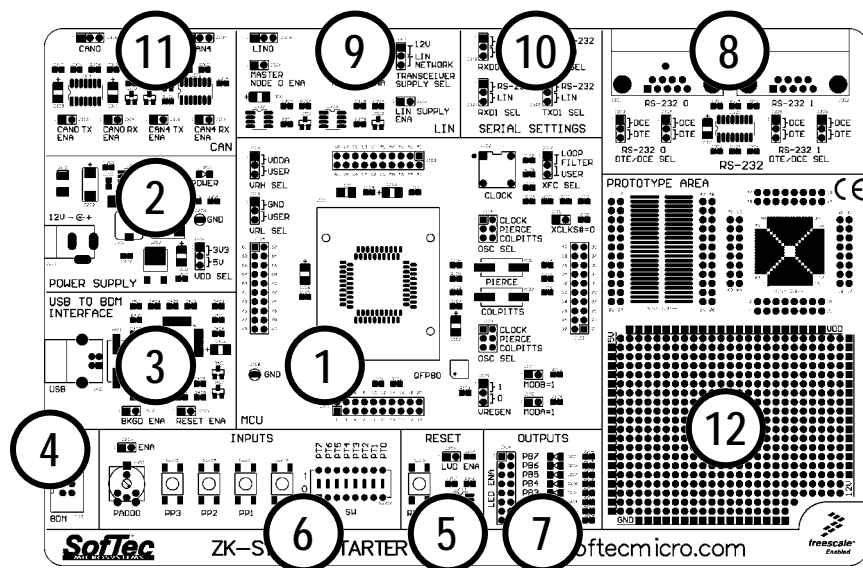
The ZK-S12-B Starter Kit

Supported Devices

The ZK-S12-B Starter Kit supports the following devices:

- MC9S12A family;
- MC9S12B family;
- MC9S12C family;
- MC9S12D family;
- MC9S12DG/DJ family;
- MC9S12GC family;
- MC9S12KC/KG/KL family;
- MC9S12XDT family (S12X core);
- And any future pin-to-pin compatible device.

Note: *the MC9S12XDT family (S12X core) requires a special version of CodeWarrior Development Studio (CodeWarrior Development Studio for HC9S12X/XGATE).*

Recommended Reading

- Freescale HCS12 microcontroller-specific datasheets;
- SK-S12(X) and ZK-S12(X) Series Starter Kit User's Manual;
- ZK-S12-B Schematic.

2. Device Configuration

Oscillator Configuration

Thanks to the 80-pin QFP ZIF socket, the ZK-S12-B Starter Kit can support different devices, as detailed in the table below.

All of the jumpers in the "MCU" section of the board must be set according to the specific microcontroller installed. Particular care must be taken in order to correctly set the oscillator configuration.

Device	Pierce Configuration		Colpitts Configuration		Clock	
	"OSC SEL" Jumpers	"XCLKS#=0" Jumper	"OSC SEL" Jumpers	"XCLKS#=0" Jumper	"OSC SEL" Jumpers	"XCLKS#=0" Jumper
MC9S12A32	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12A64	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12A128	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12A256	-	-	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12B64	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12B128	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12C32	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12C64	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12C96	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12C128	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12D32	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12D64	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12DG128	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12DG256	-	-	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12DJ64	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12DJ128	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12DJ256	-	-	"COLPITTS"	Not Installed	"CLOCK"	Installed

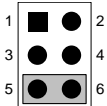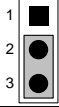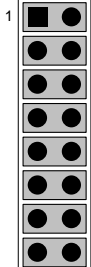| Device	Pierce Configuration		Colpitts Configuration		Clock	
	"OSC SEL" Jumpers	"XCLKS#=0" Jumper	"OSC SEL" Jumpers	"XCLKS#=0" Jumper	"OSC SEL" Jumpers	"XCLKS#=0" Jumper
MC9S12GC16	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12GC32	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12GC64	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12GC128	"PIERCE"	Installed	"COLPITTS"	Not Installed	"CLOCK"	Installed
MC9S12KC64	"PIERCE"	Installed	-	-	"CLOCK"	Installed
MC9S12KC128	"PIERCE"	Installed	-	-	"CLOCK"	Installed
MC9S12KG32	"PIERCE"	Installed	-	-	"CLOCK"	Installed
MC9S12KG64	"PIERCE"	Installed	-	-	"CLOCK"	Installed
MC9S12KG128	"PIERCE"	Installed	-	-	"CLOCK"	Installed
MC9S12KG256	"PIERCE"	Installed	-	-	"CLOCK"	Installed
MC9S12KL64	"PIERCE"	Installed	-	-	"CLOCK"	Installed
MC9S12KL128	"PIERCE"	Installed	-	-	"CLOCK"	Installed
MC9S12XDT512	"PIERCE"	Installed	-	-	"CLOCK"	Installed

Peripherals
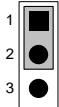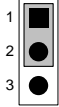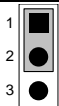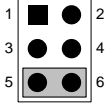
Some devices do not feature the same number of CAN and SCI peripherals. Please refer to the table below for details.

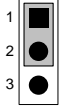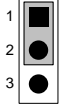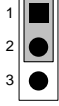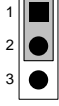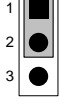| Device	CAN0	CAN1	CAN2	CAN3	CAN4	SCI0	SCI1
MC9S12A32						✓	✓
MC9S12A64						✓	✓
MC9S12A128						✓	✓
MC9S12A256						✓	✓
MC9S12B64	✓					✓	✓
MC9S12B128	✓					✓	✓
MC9S12C32	✓					✓	
MC9S12C64	✓					✓	
MC9S12C96	✓					✓	
MC9S12C128	✓					✓	
MC9S12D32	✓					✓	✓
MC9S12D64	✓					✓	✓
MC9S12DG128	✓				✓	✓	✓
MC9S12DG256	✓				✓	✓	✓
MC9S12DJ64	✓					✓	✓
MC9S12DJ128	✓				✓	✓	✓
MC9S12DJ256	✓				✓	✓	✓
MC9S12GC16						✓	
MC9S12GC32						✓	
MC9S12GC64						✓	
MC9S12GC128						✓	
MC9S12KC64	✓					✓	
MC9S12KC128	✓					✓	
MC9S12KG32	✓				✓	✓	✓
MC9S12KG64	✓				✓	✓	✓
MC9S12KG128	✓				✓	✓	✓
MC9S12KG256	✓				✓	✓	✓
MC9S12KL64	✓					✓	
MC9S12KL128	✓					✓	
MC9S12XDT512	✓	✓			✓	✓	✓

3. Summary of Jumper and Connector Settings

Jumpers Summary

Name	Reference	Description/Pinout
J105	1 ▣ 2 ● 3 ●	**VRH SELECTION** **1-2 (VDDA)** **VRH tied to VDDA (default)** 2-3 (USER) VRH connected to the J103 connector

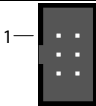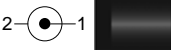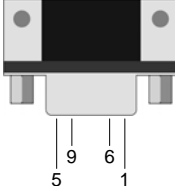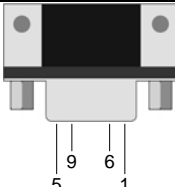| Name	Reference	Description/Pinout	
J106	1 2 3	**VRL SELECTION**	
		1-2 (GND)	**VRL tied to ground (default)**
		2-3 (USER)	VRL connected to the J104 connector
J107	1 2 3	**XFC SELECTION**	
		1-2 (LOOP FILTER)	**The on-board RC PLL loop filter is selected (default)**
		2-3 (USER)	The XFC PLL loop filter pin is connected to signal pin connector
J109	1 2 3	**VOLTAGE REGULATOR ENABLE**	
		1-2 (1)	**Microcontroller internal voltage regulator enabled (default)**
		2-3 (0)	Microcontroller internal voltage regulator disabled. VDD1, VDD2 and VDDPLL must be supplied externally.
J110		**MODA=1**	
		Installed	MODA line pulled to VDD
		Not Installed	**MODA line tied to ground by internal pull down (default)**
J111		**MODB=1**	
		Installed	MODB line pulled to VDD
		Not Installed	**MODB line tied to ground by internal pull down (default)**
J113	1 2 / 3 4 / 5 6	**OSCILLATOR SELECTION**	
		1-2 (CLOCK)	Clock selected
		3-4 (PIERCE)	Pierce crystal configuration selected
		5-6 (COLPITTS)	**Colpitts crystal configuration selected (default)**
J114	1 2 / 3 4 / 5 6	**OSCILLATOR SELECTION**	
		1-2 (CLOCK)	Clock selected
		3-4 (PIERCE)	Pierce crystal configuration selected
		5-6 (COLPITTS)	**Colpitts crystal configuration selected (default)**
J115		**XCLKS#=0**	
		Installed	XCLKS# pin pushed to ground. Full-swing Pierce crystal configuration or clock selected.
		Not Installed	**XCLKS# pulled to VDD. Loop controlled Colpitts crystal configuration selected. (default)**
J202	1 2 3	**VDD SELECTION**	
		1-2 (3V3)	VDD = 3.3 V
		2-3 (5V)	**VDD = 5 V (default)**
J204	1	**LED ENABLE**	
		Installed	**The LEDs are connected to the PB[7..0] port of the microcontroller (default)**
		Not Installed	The LEDs are not connected to the microcontroller.

| Name	Reference	Description/Pinout	
J205	■ ●	**POTENTIOMETER ENABLE**	
		Installed:	**The potentiometer is connected to the microcontroller's AN00 pin (default)**
		Not Installed:	The potentiometer is not connected to the microcontroller
J206	■ ●	**LVD ENABLE**	
		Installed:	**The LVD circuit is connected to microcontroller's RESET line (default)**
		Not Installed:	The LVD circuit is not connected to RESET line of the microcontroller
J303	1 ■ / 2 ● / 3 ●	**RS-232 0 DTE/DCE SELECTION**	
		1-2 (DCE)	**The RS-232 channel 0 is configured as DCE (default)**
		2-3 (DTE)	The RS-232 channel 0 is configured as DTE
J304	1 ■ / 2 ● / 3 ●	**RS-232 0 DTE/DCE SELECTION**	
		1-2 (DCE)	**The RS-232 channel 0 is configured as DCE (default)**
		2-3 (DTE)	The RS-232 channel 0 is configured as DTE
J305	1 ■ / 2 ● / 3 ●	**RS-232 1 DTE/DCE SELECTION**	
		1-2 (DCE)	**The RS-232 channel 1 is configured as DCE (default)**
		2-3 (DTE)	The RS-232 channel 1 is configured as DTE
J306	1 ■ / 2 ● / 3 ●	**RS-232 1 DTE/DCE SELECTION**	
		1-2 (DCE)	**The RS-232 channel 1 is configured as DCE (default)**
		2-3 (DTE)	The RS-232 channel 1 is configured as DTE
J307	■ ●	**MASTER NODE 0 ENABLE**	
		Installed	**LIN0 master node (default)**
		Not Installed	LIN0 slave node
J309	■ ●	**MASTER NODE 1 ENABLE**	
		Installed	**LIN1 master node (default)**
		Not Installed	LIN1 slave node
J311	1 ■ / 2 ● / 3 ●	**LIN TRANSCEIVER SUPPLY SELECTION**	
		1-2 (12V)	**LIN transceivers are supplied by the Starter Kit's internal 12 V DC voltage (default)**
		2-3 (LIN NETWORK)	LIN transceivers supplied by the LIN bus' VBAT line
J312	■ ●	**LIN SUPPLY ENABLE**	
		Installed	**LIN bus is powered by the Starter Kit's internal 12 V DC voltage (default)**
		Not Installed	LIN bus is self-powered

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Name	Reference	Description/Pinout	
J313	■ ●	**CAN0 TX ENABLE**	
		Installed	**Microcontroller's PM1/TXCAN0 pin connected to CAN0 transceiver (default)**
		Not Installed	Microcontroller's PM1/TXCAN0 pin floating
J314	■ ●	**CAN0 RX ENABLE**	
		Installed	**Microcontroller's PM0/RXCAN0 pin connected to CAN0 transceiver (default)**
		Not Installed	Microcontroller's PM0/RXCAN0 pin floating
J316	■ ●	**CAN4 TX ENABLE**	
		Installed	**Microcontroller's PJ7/TXCAN4 pin connected to CAN4 transceiver (default)**
		Not Installed	Microcontroller's PJ7/TXCAN4 pin floating
J317	■ ●	**CAN4 RX ENABLE**	
		Installed	**Microcontroller's PJ6/RXCAN4 pin connected to CAN4 transceiver (default)**
		Not Installed	Microcontroller's PJ6/RXCAN4 pin floating
J319	1 ■ / 2 ● / 3 ●	**RXD0 SELECTION**	
		1-2 (RS-232)	**Microcontroller's PS0/RXD0 pin connected to RS-232 0 transceiver (default)**
		2-3 (LIN)	Microcontroller's PS0/RXD0 pin connected to LIN0 transceiver
J320	1 ■ / 2 ● / 3 ●	**TXD0 SELECTION**	
		1-2 (RS-232)	**Microcontroller's PS1/TXD0 pin connected to RS-232 0 transceiver (default)**
		2-3 (LIN)	Microcontroller's PS1/TXD0 pin connected to LIN0 transceiver
J321	1 ■ / 2 ● / 3 ●	**RXD1 SELECTION**	
		1-2 (RS-232)	**Microcontroller's PS2/RXD1 pin connected to RS-232 1 transceiver (default)**
		2-3 (LIN)	Microcontroller's PS2/RXD1 pin connected to LIN1 transceiver
J322	1 ■ / 2 ● / 3 ●	**TXD1 SELECTION**	
		1-2 (RS-232)	**Microcontroller's PS3/TXD1 pin connected to RS-232 1 transceiver (default)**
		2-3 (LIN)	Microcontroller's PS3/TXD1 pin connected to LIN1 transceiver
J402	■ ●	**BKGD ENABLE**	
		Installed	**Microcontroller's BKGD line connected to the "USB TO BDM INTERFACE" (default)**
		Not Installed	Microcontroller's BKGD line not connected to the "USB TO BDM INTERFACE"

Name	Reference	Description/Pinout
J403	■ ●	**RESET ENABLE**
		Installed — **Microcontroller's RESET# line connected to the "USB TO BDM INTERFACE" (default)**
		Not Installed — Microcontroller's RESET# line not connected to the "USB TO BDM INTERFACE"

Connectors Summary/Pinout

Name	Reference	Description/Pinout
J101, J102, J103, J104		**MCU I/O Connectors** See schematic for pin explanation
J108		**Ground Test Point**
J112	1—	**BDM Connector** 1. BKGD 2. GND 3. N.C. 4. RESET# 5. N.C. 6. VDD
J201	2—●—1	**12 V DC Power Supply Input Connector** 1. 12 V DC 2. GND
J203		**Ground Test Point**
J301	9 6 / 5 1	**RS-232 0 Connector** 1. N.C. 2. TX or RX (see J303/J304 jumpers) 3. RX or TX (see J303/J304 jumpers) 4. N.C. 5. GND 6. N.C. 7. N.C. 8. N.C. 9. N.C.
J302	9 6 / 5 1	**RS-232 1 Connector** 1. N.C. 2. TX or RX (see J305/J306 jumpers) 3. RX or TX (see J305/J306 jumpers) 4. N.C. 5. GND 6. N.C. 7. N.C. 8. N.C. 9. N.C.
J308	1 3	**LIN0 Connector** 1. VBAT – LIN Bus Power Supply 2. LIN – LIN Signal 3. GND

Name	Reference	Description/Pinout
J310	1　3	**LIN1 Connector** 1.　VBAT – LIN Bus Power Supply 2.　LIN – LIN Signal 3.　GND
J315	1　3	**CAN0 Connector** 1.　CANH – CAN Differential Bus Line 2.　GND 3.　CANL – CAN Differential Bus Line
J318	1　3	**CAN4 Connector** 1.　CANH – CAN Differential Bus Line 2.　GND 3.　CANL – CAN Differential Bus Line
J401		**USB Connector** 1.　5 V DC USB Bus Power Supply Line 2.　USB D- 3.　USB D+ 4.　GND